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MULTI-PROCESSOR SYSTEM RECOVERY USING THERMTRIP SIGNAL

ABSTRACT OF THE DISCLOSURE

A method of responding to a thermal trip signal generated by a processor of a system having multiple processor nodes. If a processor overheats beyond a critical temperature, a temperature monitor receives the thermal trip signal, and turns off an enable signal to a voltage control module that control power to the processors. The temperature monitor also triggers a system reset. Upon reset, the temperature monitor ensures that all nodes, other than the node with the overheated processor, return to an operational state.

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